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2017 June New Cisco 400-151 Exam Dumps with PDF and VCE Free Updated in www.Braindump2go.com Today!100% Real Exam Questions! 100% Exam Pass Guaranteed! 1.|2017 Version New 400-151 Exam Dumps (PDF & VCE) 237Q&As Download: http://www.braindump2go.com/400-151.html 2.|2017 Version New 400-151 Exam Questions & Answers Download: https://drive.google.com/drive/folders/0B75b5xYLjSSNOUtSMndmVjV3czA?usp=sharing QUESTION 101Why is a Cisco UCS B250 server unable to support 16-GB DIMMs? A. Dual-rank DIMMs are not supported.B. 16-GB DIMMs are too slow.C. The largest DIMM an Intel 55xx or 56xx CPU can address is 32 GB.D. The memory bus would be too long.E. The blade would run too hot. Answer: C QUESTION 102Refer to the exhibit. Which two things can be determined from the SEL log output? (Choose

| 1b | 01/08/2011 19:25:18 | CIMC | Memory DDR3_P2GP2A0 | ECC #0xa6 | Upper critical - going high | Asserted | Reading 253 > Threshold 10 error | 1c | 01/08/2011 19:25:18 | CIMC | Memory DDR3_P2GP2A0 | ECC #0xa6 | Upper Non-recoverable - going high | Asserted | Reading 253 > Threshold 15 error | 1d | 01/08/2011 19:25:18 | CIMC | Platform alert | LED_BLADE_STATUS #0xb9 | LED is blinking fast | Asserted | 1e | 01/08/2011 19:25:18 | CIMC | Platform alert | LED_BLADE_STATUS #0xb9 | LED color is amber | Asserted | 1f | 01/08/2011 19:25:20 | CIMC | Memory DDR3_P2GP2A1 | ECC #0xa7 | | 21 | 01/08/2011 19:25:25 | CIMC | Memory DDR3_P2GP2A1 | ECC #0xa7 | Upper critical - going high | Asserted | Reading 253 > Threshold 10 error | 23 | 01/08/2011 19:25:25 | CIMC | Memory DDR3_P2GP2A1 | ECC #0xa7 | Upper critical - going high | Asserted | Reading 253 > Threshold 15 error | 23 | 01/08/2011 19:25:25 | CIMC | Memory DDR3_P2GP2A1 | ECC #0xa7 | Upper Non-recoverable - going high | Asserted | Reading 253 > Threshold 15 error | 24 | 01/08/2011 19:25:36 | CIMC | Memory DDR3_P2GP2A1 | ECC #0xa7 | Upper Non-recoverable - going high | Asserted | Reading 253 > Threshold 15 error | 24 | 01/08/2011 19:25:36 | CIMC | Memory DDR3_P2GP2A1 | ECC #0xa7 | Upper Non-recoverable - going high | Asserted | Reading 253 > Threshold 15 error | 25 | 01/08/2011 19:25:36 | CIMC | Memory DDR3_P2GP2A1 | ECC #0xa7 | | 25 | 01/08/2011 19:25:48 | CIMC | Memory DDR3_P2GP2A1 | ECC #0xa7 | | 25 | 01/08/2011 19:25:48 | CIMC | Memory DDR3_P2GP2A1 | ECC #0xa7 | | 25 | 01/08/2011 19:25:48 | CIMC | Memory DDR3_P2GP2A1 | ECC #0xa7 | | 25 | 01/08/2011 19:25:48 | CIMC | Memory DDR3_P2GP2A1 | ECC #0xa7 | | 25 | 01/08/2011 19:25:48 | CIMC | Memory DDR3_P2GP2A1 | ECC #0xa7 | | 25 | 01/08/2011 19:25:48 | CIMC | Memory DDR3_P2GP2A1 | ECC #0xa7 | | 25 | 01/08/2011 19:25:48 | CIMC | Memory DDR3_P2GP2A1 | ECC #0xa7 | | 25 | 01/08/2011 19:25:48 | CIMC | Memory DDR3_P2GP2A1 | ECC #0xa7 | | 25 | 01/08/2011 19:25:48 | CIMC | Memory DDR3_P2GP2A1 | ECC #0xa7 | | 25 | 01/08/2011 19:2

A. The server experienced a crash.B. The server experienced correctable memory errors.C. DIMMs in channel 2 are experiencing memory errors.D. DIMMs in channel 0 are experiencing memory errors.E. Memory errors have crossed two thresholds. Answer: BE QUESTION 103Refer to the exhibit. Which two things can be determined from SEL log output? (Choose

TWO)

1b | 01/08/2011 19:25:18 | CIMC | Memory DDR3_P2GP2A0
ECC f0xa6 | Upper critical - going high | Asserted |
Reading 253 > Threshold 10 error

1c | 01/08/2011 19:25:10 | CIMC | Memory DDR3_P2GP2A0
ECC f0xa6 | Upper Non-recoverable - going high |
Asserted | Reading 253 > Threshold 15 error

1d | 01/08/2011 19:25:18 | CIMC | Platform alert
LED_BLADE_STATUS f0xb9 | LED to blinking fast | Asserted

1e | 01/08/2011 19:25:18 | CIMC | Platform alert
LED_BLADE_STATUS f0xb9 | LED color is abbor | Asserted

20 | 01/08/2011 19:25:23 | CIMC | Memory DDR3_P2GP2A0
ECC f0xa6 |

21 | 01/08/2011 19:25:25 | CIMC | Memory DDR3_P2GP2A1
ECC f0xa6 | Upper critical - going high | Asserted |
Reading 253 > Threshold 10 error

23 | 01/08/2011 19:25:25 | CIMC | Memory DDR3_P2GP2A1
ECC f0xa7 | Upper won-recoverable - going high |
Asserted | Reading 253 > Threshold 15 error

A. The server experienced a crash.B. DIMMs in channel 2 are experiencing memory errors.C. DIMMs in channel 0 are experiencing memory errors.D. The server experienced correctable memory errors.E. Memory errors have crossed two thresholds. Answer: DE QUESTION 104Which two statements about FabricPath are true? (Choose two) A. Multi-destination trees are built to create a loop-free topology for unicast traffic.B. FabricPath interfaces send and receive Ethernet and FabricPath encapsulated packets.C. FabricPath is supported on Cisco Nexus 7000 on all module types.D. FabricPath switch should always be the root for a spanning tree network attached to it.E. Spanning tree does not run inside a FabricPath network. Answer: DE QUESTION 105Refer to the exhibit. The switch is running Fabricpath, however the CE port e1/9 is not on spanning tree forwarding. Which option describes the possible cause?



A. Switch 1 is receiving better BPDU on E1/9.B. Bridge assurance is configured on switch 1 E1/9 but not configured on the facing interface.C. Switch 1 is not configured with FabricPath.D. Loopguard feature is blocking the port. Answer: A QUESTION 106Refer to the exhibit Server 1 and server 2 are on the same VLAN but cannot reach each other. Which option

describes the reason?



A. Interface VALN (SVI) must be configured for this VLAN.B. VLAN is not allowed on the peer link.C. VLAN is not on FabricPath mode.D. Type2 inconsistency prevents the communication. Answer: CQUESTION 107FabricPath brings the benefits of routing protocols to Layer 2 network Ethernet environments. Which thee options describe advantages of using FabricPath technology? (Choose three.) A. MAC address scalability with conversational learningB. loop mitigation with TTL in the frame fieldC. Spanning Tree Protocol independenceD. 10-Gb/s bandwidthE. Layer 2 extensions between data centers Answer: ABC QUESTION 108In the Cisco Nexus 7000 FabricPath domain, one switch becomes the root for the multidestination tree. Cisco FabricPath IS-IS will automatically select root switches for each multidestination tree. One of the parameters that is used to select the root is the system ID. Which two statements are true about the system ID? (Choose two.) A. The system ID is a unique 12-bit value.B. The system ID is a 48-bit value composed of the VDC MAC address.C. A higher system ID is preferred.D. A lower system ID is preferred. Answer: BC QUESTION 109After configuring FabricPath, the following message appears in the syslog: %STP-2-L2GW_BACKBONE_BLOCK: L2 Gateway Backbone port inconsistency blocking port port-channel100 on VLAN0010. What is a possible reason for this message? A. Spanning tree must be disabled, and the message indicates that STP is enabled.B. STP is misconfigured. The root guard must be disabled.C. An inferior BPDU was received on a Cisco FabricPath edge port.D. A superior BPDU was received on a Cisco FabricPath edge port. Answer: D QUESTION 110Cisco FabricPath switches support ECMP forwarding for known unicast frames. If the destination switch ID can be reached through more than one output interface with equal cost, the forwarding engine uses a hash function to pick one of the interfaces. Which three options are valid for hash input for unicast frames? (Choose three.) A. Use only the source parameters (layer-3, layer-4, or mixed).B. Use the FabricPath switch ID in the FabricPath header.C. Use only the destination parameters (layer-3, layer-4, or mixed).D. Use both the source and the destination parameters (layer-3, layer-4, or mixed).E. Include only Layer 2 input (source or destination MAC address). Answer: ACD !!!RECOMMEND!!!1.|2017 Version New 400-151 Exam Dumps (PDF & VCE) 237Q&As Download: http://www.braindump2go.com/400-151.html 2.|2017 Version New 400-151 Study Guide Video: YouTube Video: YouTube.com/watch?v=17-eCNdml4Y